



PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E

PATENT DATE

AC SCANNED TM² Q.A. JVT

APPLICATION NO. 	CONT/PRIOR 	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/709695		703	15	2123	PALADINI

Richard Ferrer
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Memory efficient occurrence model design for VLSI CAD
DEUCAIS'98

PTO-2040
12/88

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER		DRAWINGS			CLAIMS ALLOWED	
		Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.					NOTICE OF ALLOWANCE MAILED	
					(Assistant Examiner) _____ (Date) _____	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____					ISSUE FEE	
					(Primary Examiner) _____ (Date) _____	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.					Amount Due _____ Date Paid _____	
					ISSUE BATCH NUMBER _____	
					(Legal Instruments Examiner) _____ (Date) _____	

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